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Corrigendum-1

**Date: 7/2/18**

**Ref:** IIT/SRIC/AE/STB/AP/ 2017-18/EQ4, Dated: 24.1.2018

In connection with the above mentioned tender, the following changes may be noted:

Tender Reference	<b>IIT/SRIC/AE/STB/AP/ 2017-18/EQ4, Dated: 24.1.2018</b>
Processor	2 x Intel® Xeon® Processor BDW-EP 8C E5-2609V4 1.7G 20M 6.4GT QPI Or Equivalent (in no. of cores and processor speed provided the version be next generation)
Chipset	Intel® C612 (or consistent with the version of processor above)
Ports	2x COM ports (1 rear, 1 Header),5x USB 3.0 ports (2 rear, 2 via header, 1Type A),4x USB 2.0 ports (2 rear, 2 via headers),1x VGA port,7.1 HD Audio,2x RJ45 Gigabit Ethernet LAN ports,1x RJ45 Dedicated IPMI LAN port ,10x SATA3 (6Gbps) ports ( <b>some requirements may be relaxed on a case to case basis</b> )
Chassis	Tower Chassis with appropriate power supply with at least Platinum Level ratings

**Price of Tesla GP100 16 GB GPU must be part of the bid.**

**The last date for submission of the bids has been extended to 21/2/18**

All other terms and conditions remain unaltered. For more details please refer Tender No. IIT/SRIC/AE/STB/AP/ 2017-18/EQ4, Dated: 24.1.2018