

With reference to Tender No: **IIT/SRIC/CSE/NDM/JM/18-19/EQ-2**, Dated: **03.09.2018**, a few changes have been made which are described below in this corrigendum:

1. In page 2/6 of the tender - Item description of the chipset is changed as follows:

Chipset	Intel® C620 or higher Series Chipset - Dual 1-Gigabit Ethernet - Mid Tower
---------	--

2. In page 2/6 of the tender - the following Item is modified:

Support	Hardware or Software based 24 hour support
---------	--

The last date of application may be extended to 22<sup>nd</sup> Oct, 2018.