

# Dept. of Electronics and Electrical Communication Engg.

## IIT Kharagpur

### Corrigendum

Sub: Tender for procurement of "High Performance Computation Platform"

**Enquiry No. IIT/ECE/ENQ-01/AS/ISIRD/2020-21/03**

**Date: 05/09/2020**

With reference to tender no IIT/ECE/ENQ-01/AS/ISIRD/2020-21/01 and Corrigendum no IIT/ECE/ENQ-01/AS/ISIRD/2020-21/02, the following clauses/specifications/items in the tender document may be modified as follows.

The technical specification of the Firmware has been updated and to be read from the current corrigendum. The technical specifications of all the other components of the "High Performance Computation (HPC) Platform" remain the same as mentioned in Tender no: IIT/ECE/ENQ-01/AS/ISIRD/2020-21/01 and Corrigendum no IIT/ECE/ENQ-01/AS/ISIRD/2020-21/02. The terms and conditions for procurement of the "High Performance Computation Platform" remain the same and is to be read from tender no: IIT/ECE/ENQ-01/AS/ISIRD/2020-21/01.

	<b>As given in the old tender</b>	<b>New/Updated information to be read as</b>
Firmware security	1. For firmware security, system should support remote management chip creating a fingerprint in the silicon, preventing servers from booting up unless the firmware matches the fingerprint. This feature should be immutable 2. Should maintain repository for firmware and drivers recipes to aid rollback or patching of compromised firmware. Should also store Factory Recovery recipe preloaded to rollback to factory tested secured firmware	No particular specifications for firmware

Last Date and Time for submitting the tender document	12P.M. on 2 <sup>nd</sup> October , 2020	12.00 P.M. on 2nd October, 2020
Time and Date of Opening of Technical Bids	3.00 P.M. on 5 <sup>th</sup> October, 2020	3.00 P.M. on 5 <sup>th</sup> October, 2020

Signature of the PI (Dr. Aniket Singha)





5/09/20

Signature of the H.O.D (Prof. P. K. Biswas)

