Dept. of Electronics and Electrical Communication Engg.

IIT Kharagpur

Corrigendum

Sub: Tender for procurement of "High Performance Computation Platform"

Enquiry No. IIT/ECE/ENQ-01/AS/ISIRD/2019-20/02 **Date: 03/12/2019**

With reference to tender no **IIT/ECE/ENQ-01/AS/ISIRD/2019-20/01**, the following clauses/specifications/items in the tender document may be modified as follows.

The technical specifications of the GPU card and the UPS have been updated and to be read from the current corrigendum. The technical specifications of all the other components of the "High Performance Computation (HPC) Platform" remain the same as mentioned in Tender no: **IIT/ECE/ENQ-01/AS/ISIRD/2019-20/01.** The terms and conditions for procurement of the "High Performance Computation Platform" remain the same and is to be read from tender no: **IIT/ECE/ENQ-01/AS/ISIRD/2019-20/01.**

	As given in the old tender	**New/Updated information to be read as:**
GPU card specifications	2 x NVIDIA Titan RTX 24GB PCI-e GPU At least 2 free slots for adding two extra cards in future	**2 x NVIDIA Titan RTX 24GB PCI-e GPU** or **2 x Nvidia RTX 6000 24 GB PCI-e GPU** **At least 2 free slots for adding two extra cards in future**
UPS	3KVA on-line UPS with 30 mins back up	**3 KVA online UPS with a minimum 15 minutes of backup**
Last Date and Time for submitting the tender document	12P.M. on 13th December, 2019	**12P.M. on 20th December, 2019**
Time and Date of Opening of Technical Bids	3.30 P.M. on 13th December, 2019	**3.30 P.M. on 20th December, 2019**

A Singha 03/12/19

Signature of the PI (Dr. Aniket Singha)

Signature of the H.O.D (Prof. P. K. Biswas)