



**INDIAN INSTITUTE OF TECHNOLOGY
KHARAGPUR – 721302**

No. IITKGP/ECE/ENQ/ECL/PM/08/2018-19

Date: 30/10/2018

Sub: Procurement of Universal IC Tester for Electronics Circuit Laboratory, Department of Electronics and Electrical Communication Engineering.

Indian Institute of Technology Kharagpur an Institute of National Importance, invites sealed bids from reputed **Original Equipment Manufacturers or their authorized Distributors / Dealers** who have adequate credential for supply/ installation of the items as per the Annexure – I for the Department of Electronics & Electrical Communication Engineering, Indian Institute of Technology, Kharagpur, India.

Interested vendors may submit their sealed bids under Two-cover system as per **Technical Specifications** given at **Annexure – I**, and **General Terms & Conditions** given at **Annexure – II** and **FORMAT FOR PRICE BID in Annexure – III** . Kindly refer to the Institute website: www.iitkgp.ac.in {Link: tenders & quotations} for complete tender details.

The tender be sent in a sealed packet, containing two separate sealed envelopes (one each for **Technical Bid and Price Bid**) duly superscripted with **Tender Notice No. IITKGP/ECE/ENQ/ECL/PM/08/2018-19, date:30/10/2018**, to the Office of the **Head, Department of Electronics & Electrical Communication Engineering, Indian Institute of Technology, Kharagpur, P.O.- Kharagpur Technology, Pin – 721302 West Bengal, India on or before 20th November 2018, by 4:00 PM.**

For any query pertaining to this bid document, correspondence may be addressed to - Department of Electronics & Electrical Communication Engineering, Indian Institute of Technology, Kharagpur, P.O.- Kharagpur Technology, Pin – 721302 [Ph.:03222 – 281473]

For any technical/specification related query please contact:

Prof. P. Mandal, Prof.- In- Charge, Electronics Circuit Laboratory,
Department of Electronics & Electrical Communication Engineering, Indian
Institute of Technology, Kharagpur, P.O.- Kharagpur Technology,
Pin – 721302, Ph. No.- 03222-283562

The technical bid will be evaluated first and price bids will be opened only in respect of those OEMs/Vendors, who are found technically qualified after evaluation of Technical bids.

The **technical bids will be opened on 22nd November, 2018, at 11:00 AM in the Office of the Head, Department of Electronics & Electrical Communication Engineering, Indian Institute of Technology, Kharagpur,**

Encl: Annexure – I, II and III

To
The Prof.-In-Charge
Institute Website


Head, Department of E& EC E,
IIT Kharagpur,

Professor & Head
E & E.C.E. Dept.
I.I.T., Kharagpur

Pradyip Mandal
30.10.18

ANNEXURE – I

Description and Specification of the stores:

Universal IC tester along with standard features, optional feature and Technical specifications.

Standard Features:

1. Automatically test IC within seconds
2. Conversational mode for testing ICs , simply by keying in IC number which immediately displays Result of test.
3. Test ICs without use of reference IC.
4. Alphanumeric keyboard with single key command for testing every function of the IC
5. Exhaustive functional tests for the complete functions of IC, with pre-programmed logic Pattern in multiple loops.
6. High program memory capacity to store the testing software and directory for all types of ICs
7. Separate 40 pin universal ZIF socket to test LINEAR and DIGITAL IC
8. Expandable software for testing new ICs .
9. De-energise (Potential frees) test(ZIF) socket when not testing.
10. In-built protective interlocks to avoid damage against any fault to IC under test and wrong Usage of system or wrong insertion of IC
11. Highly reliable, spill –proof sealed membrane keyboard with low contact resistance
12. It has auto search facility for digital ICs
13. User memory (RAM) capacity expandable facility.
14. Buzzer to indicate bad ICs
15. Step mode to test by step
16. Serial interface RS 232-c / USB
17. Testing of LCD displays
18. Help message through key

Technical Specifications:

1. Tests a wide range of digital IC's such as 74 series, 40/45 series of COMS IC's
2. It tests a wide range of Analog ICs and RAMs (2114 IC)

The IC Tester should test The following list of ICs:

Digital ICs(TTL series):

7400 7401 7402 7403 7404 7405 7406 7407 7408 7409 7410 7411 7412 7413 7414 7417 7418 7419 7420
7421 7422 7425 7426 7427 7428 7430 7432 7433 7437 7438 7439 7440 7442 7443 7444 7445 7446 7447
7448 7449 7450 7451 7453 7454 7462 7470 7471 7472 7473 7474 7475 7476 7478 7483 7485 7486 7489
7490 7491 7492 7493 7495 7496 7497 74107 74109 74112 74116 74121 74122 74123 74125 74126 74128
74132 74133 74134 74135 74136 74137 74138 74139 74140 74141 74145 74147 74148 74150 74151
74152 74153 74154 74155 74156 74157 74158 74159 74160 74161 74162 74163 74164 74165 74166
74168 74169 74170 74172 74173 74174 74175 74176 74177 74180 74181 74182 74183 74184 74185
74189 74190 74191 74192 74193 74194 74195 74196 74197 74198 74221 74238 74240 74241 74242
74243 74244 74245 74246 74247 74248 74249 74251 74253 74256 74257 74258 74259 74260 74266
74273 74279 74280 74283 74290 74293 74298 74299 74322 74340 74341 74344 74347 74348 74350
74351 74352 74353 74354 74365 74366 74367 74368 74373 74374 74375 74377 74378 74379 74381
74382 74386 74390 74393 74395 74398 74399 74412 74423 74425 74426 74445 74447 74490 74521
74534 74540 74541 74543 74544 74563 74564 74573 74574 74575 74577 74580 74589 74590 74591
74595 74596 74620 74621 74622 74623 74638 74639 74640 74641 74642 74643 74645 74646 74647
74648 74649 74652 74668 74669 74670 74688 74689 74786 74800 74802 74804 74805 74808 74832
74841 74874 74901 74902 74903 74904 74906 74C923 74C925 74C926 74C927 74C928 74C929 74989
741244 741245 741623 741621 741639 741640 741641 741642 741643 741644 741645 743037 743038
74H01 74LS51 74H54 74L54 74L71 74H71 74LS73 74LS76 74LS78 74L85 74L86 74C89 74L93 74L95 74LS107

CMOS (40/45 XX Series) 4000 4001 4002 4006 4007 4008 4009 4010 4011 4012 4013 4014 4015 4017 4018
4019 4020 4021 4022 4023 4024 4025 4026 4027 4028 4029 4030 4031 4032 4033 4034 4035 4038 4040
4041 4042 4043 4044 4047 4048 4049 4050 4054 4055 4056 4060 4063 4067 4068 4069 4070 4071 4072
4073 4075 4076 4077 4078 4081 4082 4085 4086 4093 4094 4095 4096 4098 4099 40105 40106 40107
40109 40147 40160 40161 40162 40163 40174 40175 40181 40182 40192 40193 40194 40195 40244
40245 40257 40373 40374 40097 40098 4490 4502 4503 4504 4506 4507 4508 4510 4511 4512 4514 4515
4516 4518 4519 4520 4522 4526 4527 4528 4531 4532 4534 4539 4541 4544 4555 4556 4558 4562 4566
4584 4585 4599 4801 5801

MEMORY :

2102 2114 2115 2125 2149 6116 6264 62256 621024 9101 91L22 93412 93422 93425 4256 41256 5025

SEVEN SEGMENT DISPLAY:

LT542 LT543

LINEAR IC'S :

ANALOG TO DIGITAL CONVERTER

ADC0800 ADC0801 ADC0802 ADC0803 ADC0804 ADC0805 ADC0808 ADC0816 ADC0817 AD574 7109 3162
ADC0809

OP-AMP :

AD548 AD648 AD707 AD708 AD711 AD712 AD744 AD746 AD821 AD845 AD846 AD847 AD848 AD849
CA081 CA082 CA307 CA3100 CA3130 CA3140 CA3193 CA3240 CA3160 CA3260 LF 351 353 386 412 442 LM
10 11 101 107 108 112 118 143 155 156 157 158 201 207 208 212 218 255 256 257 258 301 307 308 312
318 343 355 356 357 358 411 441 530 531 532 538 714 725 741 748 1458 1558 2904 3080 33181 4250
4558 5204 5205 5230 5512 5532 5534 5535 13741 17301 17741 17458 17558 OP05 OP07 OP21 OP27 OP37
OPA121 OPA602 OPA606 OPA11 TL061 TL062 TL071 TL072 TL081 TL082 LT1013 709 1709 7611 7612 7621
AD713 AD840 AD841 AD842 CA084 LM 11 101 107 108 112 118 124 147 148 149 201 207 208 212 218 224
248 249 301 307 308 312 318 324 347 348 349 442 444 534 725 741 747 748 2900 2902 3303 3403 3503
5514 17301 17741 TL064 TL074 TL084 OPA404 7641 7642 LT1014 524 5533
(OPAMP AND COMPARATOR: 192 292 392 2924)

COMPARATOR :

106 111 160 193 211 260 293 311360 393 2903 17393 CA3290 111 119 139 161 211 219 239 261 311 319
339 361 2901 3302 17339 521 522 527 529 2111 2211 2311

ANALOG SWITCH:

4016 4051 4052 4053 4066 6108 6208 11201 11202 11331 11508 11509 13201 13202 13331 11332 13332
13508 13509 14016 14051 14052 14053 14066 DG200 DG201 DG202 DG308 DG309 DG211 DG212 DG506
DG507 DG508 DG509

WAVEFORM GENERATOR: 8038

TIMER:

555 556 7555 PLL 565 567N 4046

VCO:

131 231 331 131A 231A 331A 4151 566N

VOLTAGE REGULATOR:

7805 78L05 78M05 78T05 7806 7808 7812 7815 7824 7905 79L05 79M05 7906 7908 7915 7918 117 117L
117M 217 217L 217M 317 317L 317M 337 18512 28512 38512 18525 28525 38525 2930 2931 723

3. Test by: Truth tables /sequence table comparisons.
4. Display: 16x2 Backlit LCD Display
5. Electrical Requirement: 230(+/- 10%), 1 phase, 50Hz

ANNEXURE – II

Sealed bids are invited for procurement of the following items from the reputed **Original Equipment Manufacturers or their authorized Distributors / Dealers** who have adequate credential for the Department of Electronics & Electrical Communication Engineering, Indian Institute of Technology, Kharagpur, India.

01. Universal IC Tester 2.Quantity : 2 numbers , as per Specifications in Annexure – I

Last date of Submission of sealed Bids: 20th November, 2018 ,4:00 PM.

PLEASE SPECIFICALLY INDICATE THE FOLLOWING POINTS IN YOUR QUOTATIONS AND COMPLY THE TERMS AS MENTIONED HERE UNDER

- (1) TENDERS ARE INVITED COMPLYING THE REQUIREMENT FOR TENDER AS DETAILED IN THE TENDER SPECIFICATION TO BE SUBMITTED IN THE COMPANY'S/FIRM'S LETTERHEAD NEATLY PRINTED/TYPED DULY SIGNED BY AUTHORIZED PERSON WITH THE SEAL OF THE BIDDERS. ALL ENVELOPS CONTAINING THE TENDER SHOULD BE PROPERLY SEALED.SEPARATE SEALED ENVELOPS SHOULD BE USED FOR TECHNICAL AND PRICE BID AND INDICATION TO THEIR EFFECT MAY PLEASE BE SUPERSCRIBED ON THE ENVELOP.
- (2) **TECHNICAL CATALOGUE/LEAFLET SHOULD BE ENCLOSED WITHOUT FAIL.**
- (3) PLEASE CONFIRM WHETHER YOU ARE AUTHORISED TO QUOTE ON BEHALF OF YOUR PRINCIPALS AND IF SO, PLEASE ENCLOSE A COPY OF SUCH AUTHORISATION WITH YOUR QUOTATION.
- (4) PRICE BIDS: PRICES ARE TO BE QUOTED FOR DOOR DELIVERY AT IIT KHARAGPUR, CLEARLY SHOWING THE GST RATE * .ALL PRICES SHOULD BE QUOTED ONLY IN INDIAN RUPEES.
* (NOTE: GST WILL BE APPLICABLE 5% AS PER GOVT. NORMS & CERTIFICATE WILL BE PROVIDED IF REQUIRED).
- (5) PERIOD OF VALIDITY: BIDS SHALL REMAIN VALID FOR ACCEPTANCE FOR A PERIOD OF 120 DAYS FROM THE DATE OF OPENING.
- (6) SCOPE OF SUPPLY: SHOULD INCLUDE FREE INSTALLATION AND COMMISSIONING.
- (7) PAYMENT TERMS
 - A) 90% PAYMENT SHALL BE RELEASED AFTER RECEIPT OF GOODS IN GOOD ORDER AND CONDITION AND SUCCESSFUL INSTALLATION, THEREAFTER CERTIFICATE FROM THE CONCERNED HEAD OF THE DEPARTMENT AND BALANCE 10% PAYMENT SHALL BE RELEASED AFTHET SUBMISSION OF PERFORMANCE BANK GUARANTEE (PBG) AMOUNTING TO 10% OF THE PURCHASE ORDER VALUE. THE PBG WILL BE DRAWN FROM ANY NATIONAL/COMMERCIAL BANK VALID FOR THE WARRANTY PERIOD PLUS SIXTY DAYS.
 - B) ENSURE MENTIONING
 - (i) BANK DETAILS OF THE BENEFICIARY AND PAN NUMBER
 - (ii) FULLNAME AND ADDRESS OF THE BENEFICIARY ON WHOM ORDER HAS TO BE PLACED.
- (8) WHETHER ANY EXPORT LICENCE IS REQUIRED FROM YOUR GOVERNMENT, IF SO, PLEASE CONFIRM WITH DETAILS.

- (9) COUNTRY OF ORIGIN OF THE GOODS IS TO BE MENTIONED.
- (10) THE EXCISE DUTY EXEMPTION (10/97)/CUSTOM DUTY EXEMPTION (51/96) CERTIFICATE WILL BE PROVIDED BY THE INSTITUTE ON REQUEST BY THE VENDOR.
- (11) LIQUIDATED DAMAGES: THE STORES SHOULD BE DELIVERED/DESPATCHED TO DESTINATION AND READY FOR OPERATION NOT LATER THAN THE DELIVERY DATE SPECIFIED. IF THE SUPPLIER FAILS TO DELIVER ANY OR ALL THE STORES OR PERFORM THE SERVICE BY THE SPECIFIED DATE, LIQUIDATED DAMAGE SAT 1% PER MONTH OR PART THERE OF IN RESPECT OF THE VALUE OF STORES WILL BE DEDUCTED FROM THE CONTRACT PRICE SUBJECT TO A MAXIMUM OF 5% .ALTERNATIVELY, THE ORDER WILL BE CANCELLED AND THE UNDELIVERED STORES PURCHASED FROM ELSEWHERE AT THE RISK AND EXPENSE OF SUPPLIER.
- (12) PATENT RIGHTS: THE SUPPLIER SHALL INDEMNIFY THE PURCHASE AGAINST ALL THIRDPARTY CLAIMS OF INFRINGEMENT OF PATENT, TRADEMARK OR INDUSTRIAL DESIGN RIGHTS ARISING FROM USE OF THE GOODS OR ANY PART THERE OF IN INDIA.
- (13) ONLY THOSE BIDDERS WHOSE BIDS HAVE BEEN TECHNICALLY FOUND ACCEPT ABLE WILL BE INVITED FOR PARTICIPATION IN THE PRICE BID.
- (14) THOSE BIDDERS WHO DO NOT RECEIVE ANY COMMUNICATION FOR PARTICIPATION IN PRICE BID OPENING MEETING MAY PRESUME THAT THEIR BID HAS NOT BEEN ACCEPTED BY THE INSTITUTE.
- (15) CONDITIONAL OFFER WILL NOT BE ACCEPTED.
- (16) LATE TENDERS, TENDER RECEIVED AFTER THE DUE DATE AND TIME OF SUBMISSION AS MENTIONED ABOVE SHALL NOT BE ACCEPTED.
- (17) BIDDERS TO ENCLOSE THE FOLLOWING DOCUMENTS:-
- A) CURRENT YEAR INCOME TAX CLEARANCE CERTIFICATE.
 - B) PAN NO.
- (18) WARRANTY / GUARANTEE: THIS COMPREHENSIVE ONSITE WARRANTY / GUARANTEE SHALL REMAIN VALID FOR **36 MONTHS** AFTER THE GOODS (OR ANY PORTION THEREOF AS THE CASE MAY BE) HAVE BEEN DELIVERED AND COMMISSIONED TO THE FINAL DESTINATION INDICATED IN THE CONTRACT.
- (19) THE INSTITUTE DOES NOT BIND ITSELF TO OFFER ANY EXPLANATION TO THOSE BIDDERS WHOSE TECHNICAL BID HAS NOT BEEN FOUND ACCEPTABLE BY THE EVALUATION COMMITTEE OF THE INSTITUTE.
- (20) ALL TENDERS (UNLESS OTHERWISE SPECIFIED) ARE TO BE SUBMITTED / HANDEDOVER TO THE HEAD, DEPARTMENT OF ELECTRONICS AND ELECTRICAL COMMUNICATION ENGINEERING, INDIAN INSTITUTE OF TECHNOLOGY, KHARAGPUR – 721302.
- (21) VENDOR CODE SHOULD BE MENTIONED.

IMPORTANT

- (a) IIT Kharagpur authority may accept or reject any or all the bids in part or in full without assigning any reason and does not bind itself to accept the lowest bid. The Institute at its discretion may change the quantity / upgrade the criteria/ drop any item or part thereof at any time before placing the Purchase Order.
- (b) Promptly make arrangements for repair and /or replacement of any damaged item(s) irrespective of settlement of claim.
- (c) In case of any dispute, the decision of the Institute authority shall be final and binding on the bidders.

For any query pertaining to this bid document correspondence may be addressed to Prof-In-Charge, Electronics Circuit Laboratory, Department of Electronics & Electrical Communication Engineering, Indian Institute of Technology, Kharagpur, P.O.-Kharagpur Technology, Pin – 721302.

- 1) *Please Note that the Institute remains closed during Saturdays & Sundays and all specified Institute holidays.*
- 2) *Fax, e-mail Tender will not be accepted.*
- 3) *The General Terms and Conditions as stated above relate to supply of stores /equipment /assets etc. and for specific service other terms and conditions of the Institute will apply.*

ANNEXURE – III

FORMAT FOR PRICE BID

Format to be filled up by the Agency

Tender No.....

Dated.....

1. Name of Tender:

2. Name of Firm/Bidder:

3. Address:.....

4. Phone/ Fax/ Mobile/ Email:.....

SL.NO. (a)	Description (b)	UnitBasic Price (inRs.) (c)	GST (in Rs.) (d)	Total(All Inclusive)(inRs.) (e=c+d)
1.	Brand: _____ Model : _____			

**Signature of the bidder with
seal**

ANNEXURE – III

FORMAT FOR PRICE BID

Format to be filled up by the Agency

Tender No.....

Dated.....

1. Name of Tender:

2. Name of Firm/Bidder:

3. Address:.....

4. Phone/ Fax/ Mobile/ Email:.....

SL.NO. (a)	Description (b)	UnitBasic Price (inRs.) (c)	GST (in Rs.) (d)	Total(All Inclusive)(inRs.) (e=c+d)
1.	Brand: _____ Model : _____			

**Signature of the bidder with
seal**