



Indian Institute of Technology, Kharagpur
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CORRIGENDUM/ ADDENDUM: IIT/PHY/COMP/17-18/3/A

28/08/2017

Ref: Tender Notice No. *IIT/PHY/COMP/17-18/3*, dated: 23/08/2017

Subject: HPC solution

Please note the following changes in the tender document as under:

Sr No.	Item and Page No.	In place of	Please read as
1	Page 2, Master Node and Compute node Specs	Slots: Minimum 4 PCIe 3.0 slots on motherboard without the use of riser cards or higher	Slots: Minimum 4 PCIe 3.0 slots on motherboard with or without the use of riser cards or higher
2	Page 2, Master Node Specs	Form Factor: 1U Rack form factor with rail kit for 19" rack mount	Form Factor: 2U Rack form factor with rail kit for 19" rack mount
3	Page 2, Compute node Specs	Slots: Minimum 10 PCIe 3.0 slots on motherboard without the use of riser cards or higher	Slots: Minimum 10 PCIe 3.0 slots on motherboard with or without the use of riser cards or higher
4	Page 2, <i>Network Card</i> for master and compute nodes	Not present	Infiniband: single port QDR IB to be added in the existing tender
5	Page 4, Bidders and OEM Qualifying Criteria	Proposed OEM Hardware should have at least 10 entry in top 500 org in the latest June 2013 release	Proposed OEM Hardware should have at least 10 entry in top 500 org in the latest June 2017 release

All the prospective bidders are requested to quote accordingly. The terms & conditions and all other points of specifications shall remain unaltered.